

Berg  
Serial no. 10/045,925  
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Page 5

In the claims:

1. (currently amended) A method for handling resources of a multiprocessor system, comprising the steps of:  
a requestor group of one or more processors of the system allocating a resource of a target group of one or more processors of the system, the target being interconnected to the requestor, the resource being associated with a request of the requestor, allocating the resource comprising:  
the requestor sending a request to a central crossbar interconnecting the requestor and the target and separate from the requestor and the target;  
the central crossbar allocating the resource at the target group;  
the requestor sending a result of the request to the target; and  
the target directly receiving the result by using the resource without intermediate handling of the resource by the target.
2. (cancelled)
3. (currently amended) The method of claim [[2]] 1, further comprising the central ~~hardware device~~ crossbar assigning an identifier to the request.
4. (original) The method of claim 1, further including the step of the target deallocating the resource.
5. (currently amended) The method of claim 1, wherein the resource controls access to a portion of a shared system memory of the target. [[.]]

Berg  
Serial no. 10/045,925  
Filed 1/9/2002  
Attorney docket no. BEA920000022US1

---

Page 6

6. (original) The method of claim 1, wherein the request is associated with a transaction between the requestor and the target.
7. (original) The method of claim 6, wherein the resource is a transaction identifier of the transaction.
8. (original) The method of claim 1, wherein the target comprises a first group and a second group of the processors, the first and second groups being interconnected.
9. (currently amended) A computing system, comprising:
  - a requestor group of one or more processors;
  - a resource allocator responsive to requests of the requestor; and
  - a target group of one or more processors, the target being interconnected to the requestor and responsive to a resource provided by the resource allocator and to a result of the request from the requestor;

a central crossbar interconnecting the requestor and the target and separate from the requestor and the target, the central crossbar comprising the resource allocator and requesting the resource at the target group;

wherein the target directly receives the result responsive to the ~~the~~ resource without intermediate handling of the resource by the target.
10. (cancelled)
11. (currently amended) The system of claim ~~[[10]]~~ 9, wherein the resource allocator comprises one or more pipelines for handling requests.

Berg  
Serial no. 10/045,925  
Filed 1/9/2002  
Attorney docket no. BEA920000022US1

---

Page 7

12. (cancelled)

13. (currently amended) The system of claim [[12]] 2, wherein the central ~~hardware device~~ crossbar comprises a first crossbar for data interconnection and a second crossbar for control interconnection.

14. (original) The system of claim 13, wherein the second crossbar is a tag and address crossbar.

15. (cancelled)

16. (original) The system of claim 9, wherein the resource controls allocation of a portion of shared system memory of the target.

17. (original) The system of claim 9, wherein the resource is a transaction identifier.

18. (original) The system of claim 9, wherein the target group of processors comprises interconnected first and second groups of processors.

19. (cancelled)

20. (currently amended) In a multiprocessor computer system comprising a plurality of processing nodes; a shared, distributed system memory; and a central hardware device comprising a communication pathway connecting said processing nodes; wherein each one of said processing

Berg  
Serial no. 10/045,925  
Filed 1/9/2002  
Attorney docket no. BEA920000022US1

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Page 8

nodes includes at least one processor; and a portion of said shared system memory coupled to said processor and said communication pathway; said communications pathway comprised of communications ports each dedicated to communicating with one of said processing nodes wherein said communications pathway is separate from and interconnects said processing nodes and is further comprised of a tag and address crossbar to communicate tag and address information, and a data crossbar means to communicate data within the system;

a method for handling tagging and addressing within the system comprising the steps of:

a first node communicating to said tag and address crossbar a request for a transaction in the system;

allocating a transaction identifier in the tag and address crossbar for use by the identified transaction;

conveying said transaction identifier to the originating node;

attaching said transaction identifier to the said transaction; and,

communicating said transaction from the first node to the node which receives the transaction directly through said data crossbar means.

21. (original) The method of claim 20, wherein the step of conveying said transaction identifier to the originating node is accomplished by conveying said identifier from said tag and address crossbar to said first node.

22. (currently amended) In a multiprocessor computer system capable of being partitioned into one or more independently functioning processing systems comprising:

a plurality of processing nodes, each node capable of operating independently;

a shared, distributed system memory;

and a communications pathway which interconnects said plurality of processing nodes;

Berg  
Serial no. 10/045,925  
Filed 1/9/2002  
Attorney docket no. BEA920000022US1

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Page 9

wherein each one of said processing nodes includes at least one processor and a portion of said shared system memory coupled to said processor and said communication pathway, and said communication pathway is comprised of a central ~~hardware device~~ crossbar including tag and address means to communicate the identification of data transactions being processed through the system connected to said plurality of processing nodes, said tag and address means including means to store information related to the identification of data including the target location for said data; and means to define to which memory location in one or more of said nodes said data should be sent; the central crossbar interconnecting the nodes and is separate from the nodes;

a method for handling a request from a requestor node of the system to a target node of the system comprising the steps of:

- the requestor node sending a request to the central ~~hardware device~~ crossbar;
- the central ~~hardware device~~ crossbar allocating a resource to receive results of the request at the target node;
- the central ~~hardware device~~ crossbar assigning an identifier to the request;
- the requestor sending the results to the target node; and
- the target node receiving the results directly into the resource without intermediate buffering of the results at the target node.

23. (original) The method of claim 22, wherein said target node is comprised of more than one additional node.